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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Confirmation No. 3654

Govind MALALUR et al.

Group Art Unit: 2661

Serial Number: 09/841,189

Examiner: Brian D. NGUYEN

Filed: April 25, 2001

Atty. Docket No.: 58268.00000

For: GIGABIT SWITCH ON CHIP ARCHITECTURE

RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 10, 2005

Sir:

In response to the Office Action dated July 8, 2005, please consider the remarks as set forth below.

Remarks are submitted on page 2.